Design Constraints Description Language (DCDL)

- Open standard
- Syntax, semantics, & concepts
- Tool & language independent
- Next generation design intent
- TCL compatible
- IEEE process underway

All Constraint Domains
- System Level
- Timing
- Power
- Signal Integrity
- Test
- Logic Architecture
- Environment
- Operating Points
- Physical . . .

All Design Flows
- ASIC
- FPGA
- IC
- PCB
- MCM
- SOC
- .
- .

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Why DCDL?

- Single method to capture & exchange design intent for all design flows
- Open industry collaboration ensures interoperability & leading edge support
- Covers all constraint domains, allowing full specification of design intent - at any level
- Fully defined semantics ensures consistent behavior between tools
DCDL Milestones

Year 2000

- V1.0 of the specification
- Proof of concept pilot project
- Browser-driven getting started Lift Kit
- Golden syntax parser
Need More Information?

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